



IFW

PATENT
Customer No. 22,852
Attorney Docket No. 04329.3137

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Kouji MATSUO) Group Art Unit: 2815
Application No.: 10/658,371) Examiner: Wilson, Allan R.
Filed: September 10, 2003)
For: SEMICONDUCTOR DEVICE)
INCLUDING METAL INSULATOR)
SEMICONDUCTOR FIELD)
EFFECT TRANSISTOR AND)
METHOD OF MANUFACTURING)
THE SAME)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

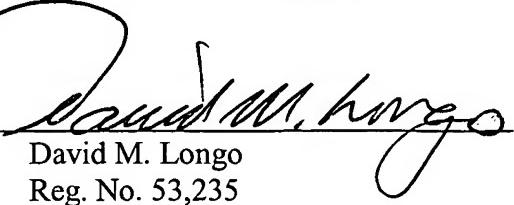
RESPONSE TO RESTRICTION REQUIREMENT

In the Restriction Requirement mailed November 19, 2004, with a period for response extending to December 20, 2004 (December 19th being a Sunday), the Examiner required restriction under 35 U.S.C. § 121 between Group I (claims 1-13) and Group II (claims 14-21). Applicant provisionally elects to prosecute Group I, claims 1-13, characterized by the Examiner as drawn to semiconductor device, without traverse.

Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By: 
David M. Longo
Reg. No. 53,235

Dated: December 15, 2004